

Fig. 1

2/11

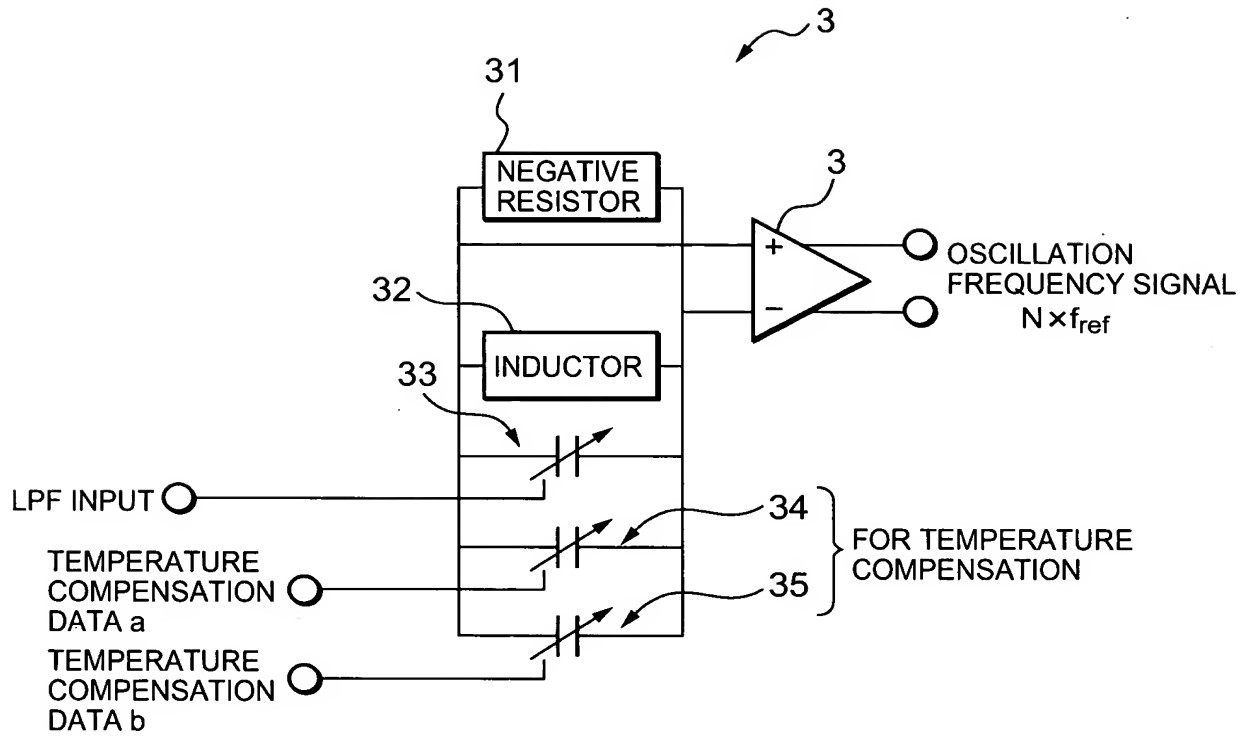


Fig. 2

3/11

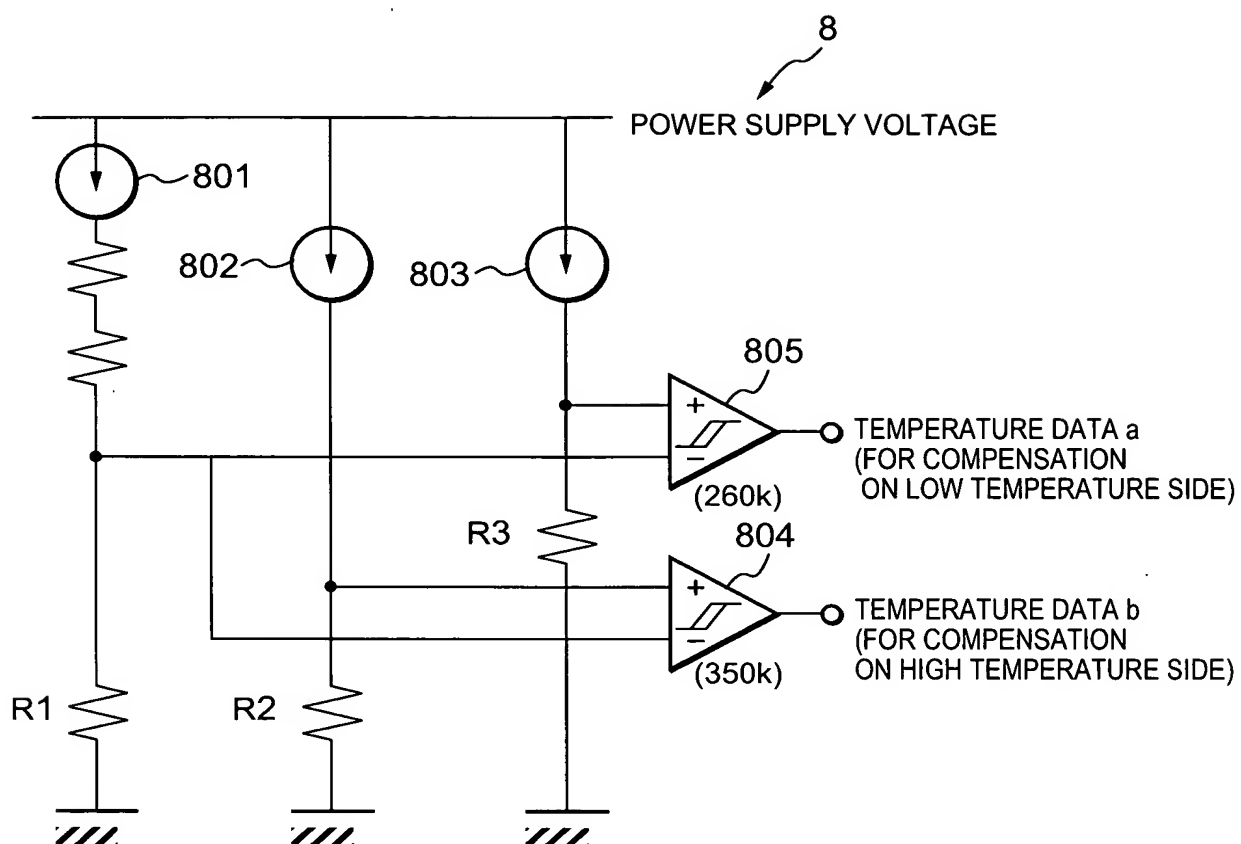


Fig. 3(a)

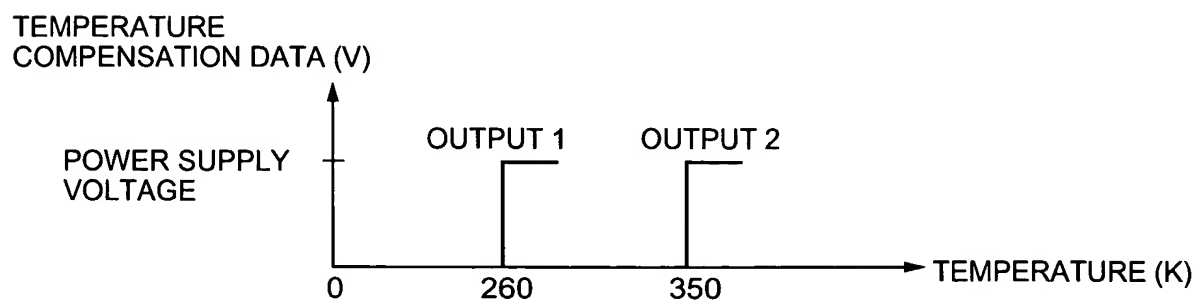


Fig. 3(b)

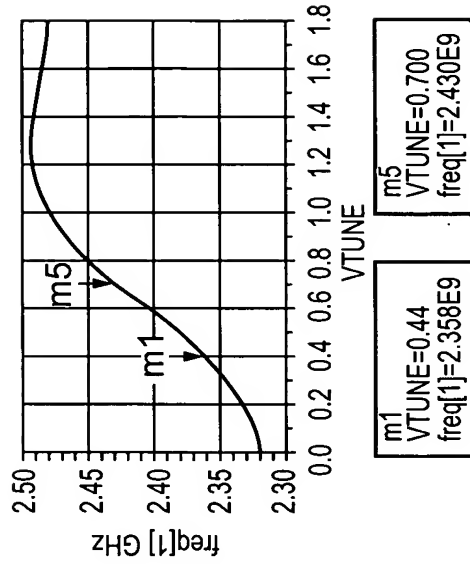


Fig. 4(c)

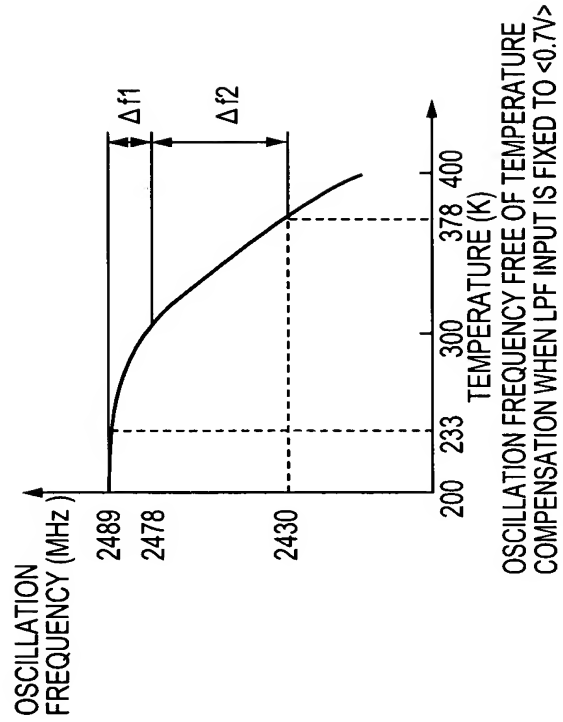


Fig. 4(d)

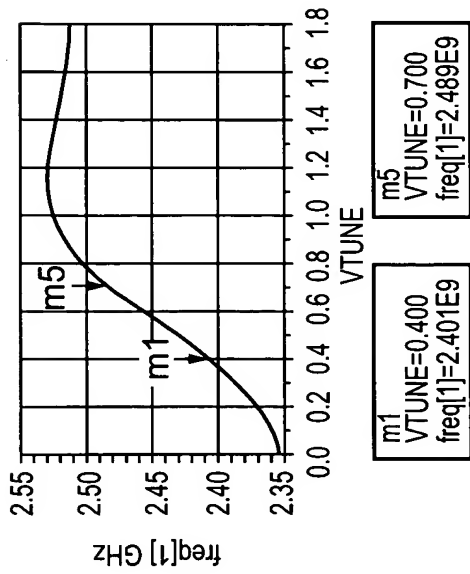


Fig. 4(a)

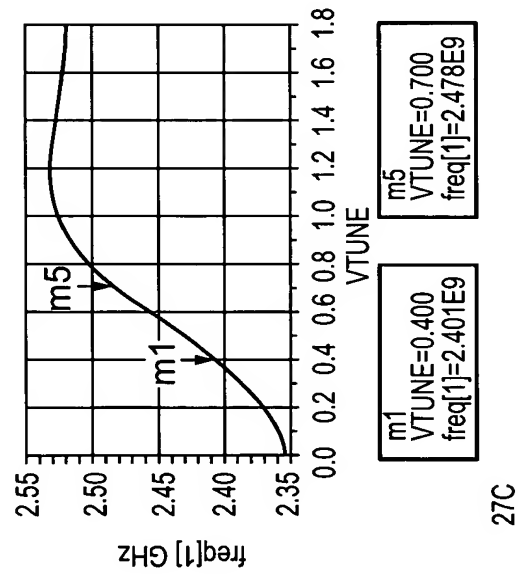


Fig. 4(b)

5/11

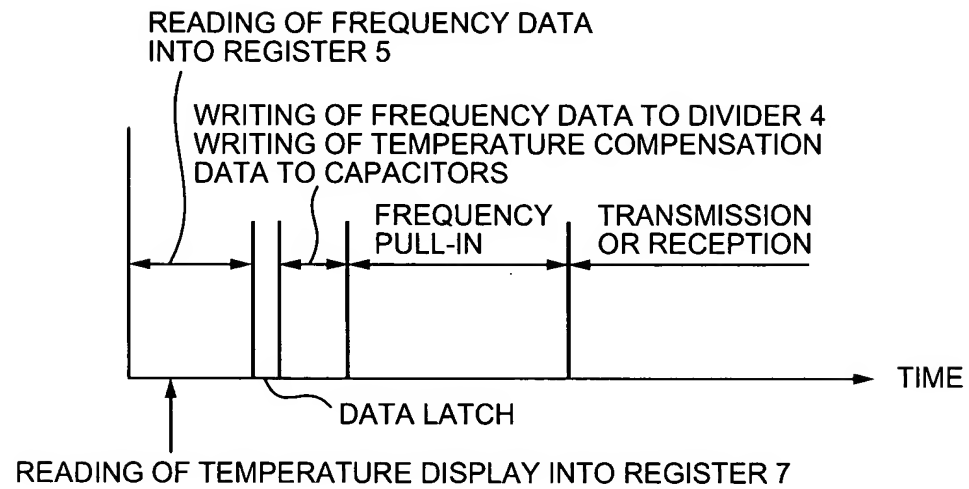


Fig. 5(a)

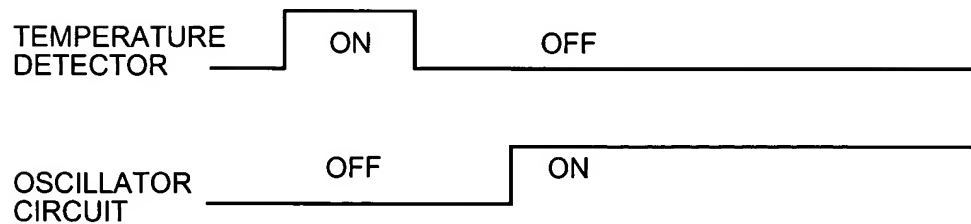


Fig. 5(b)

6/11

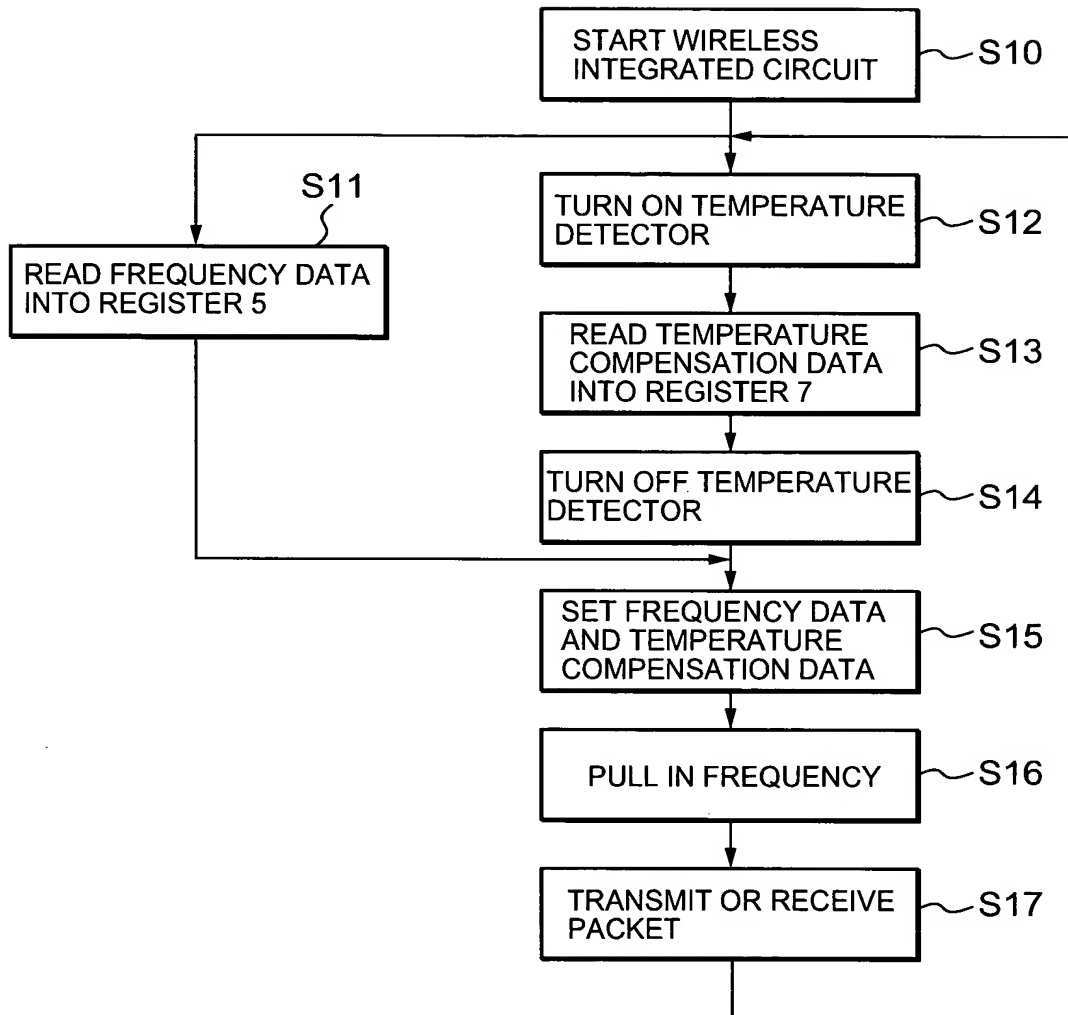


Fig. 6

7/11

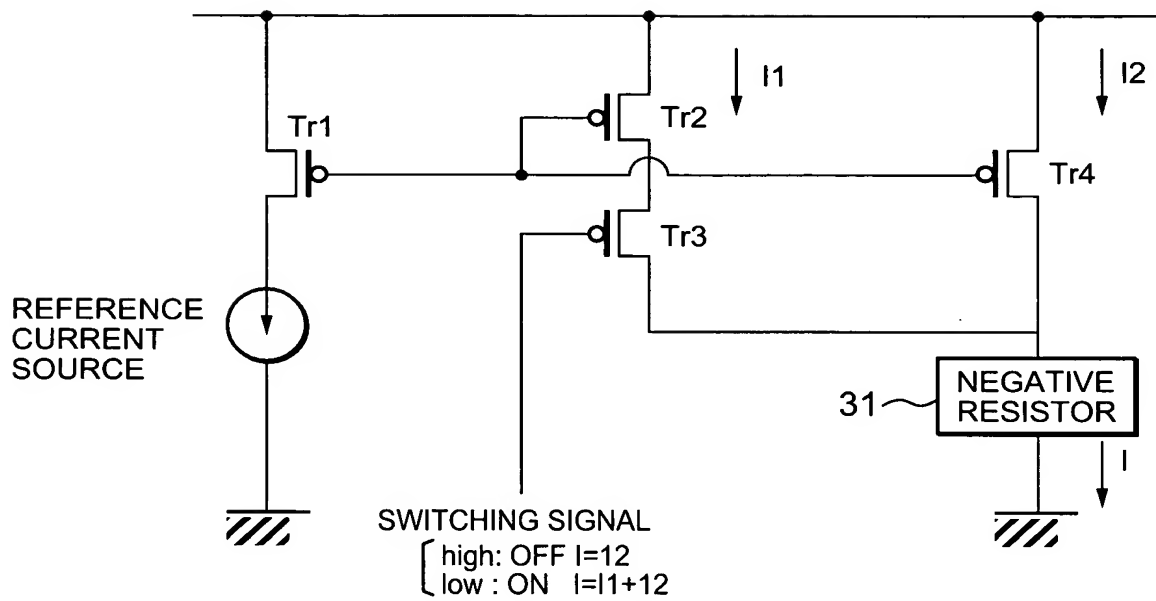


Fig. 7

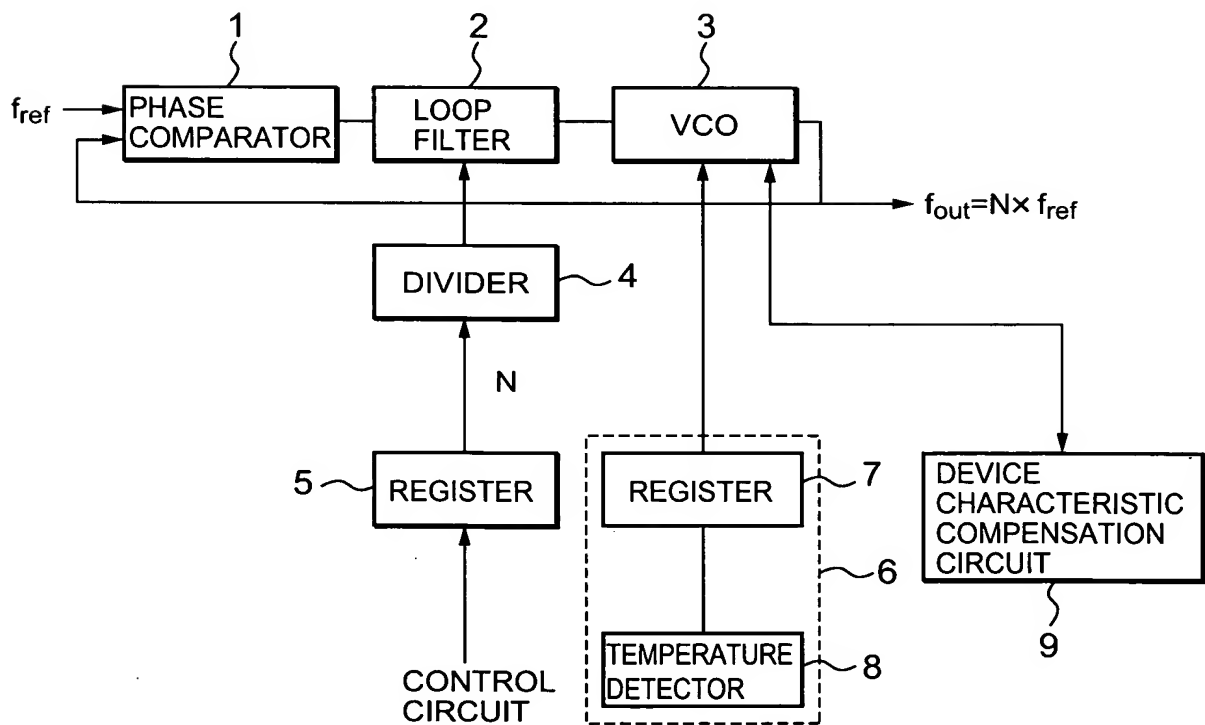


Fig. 8

8/11

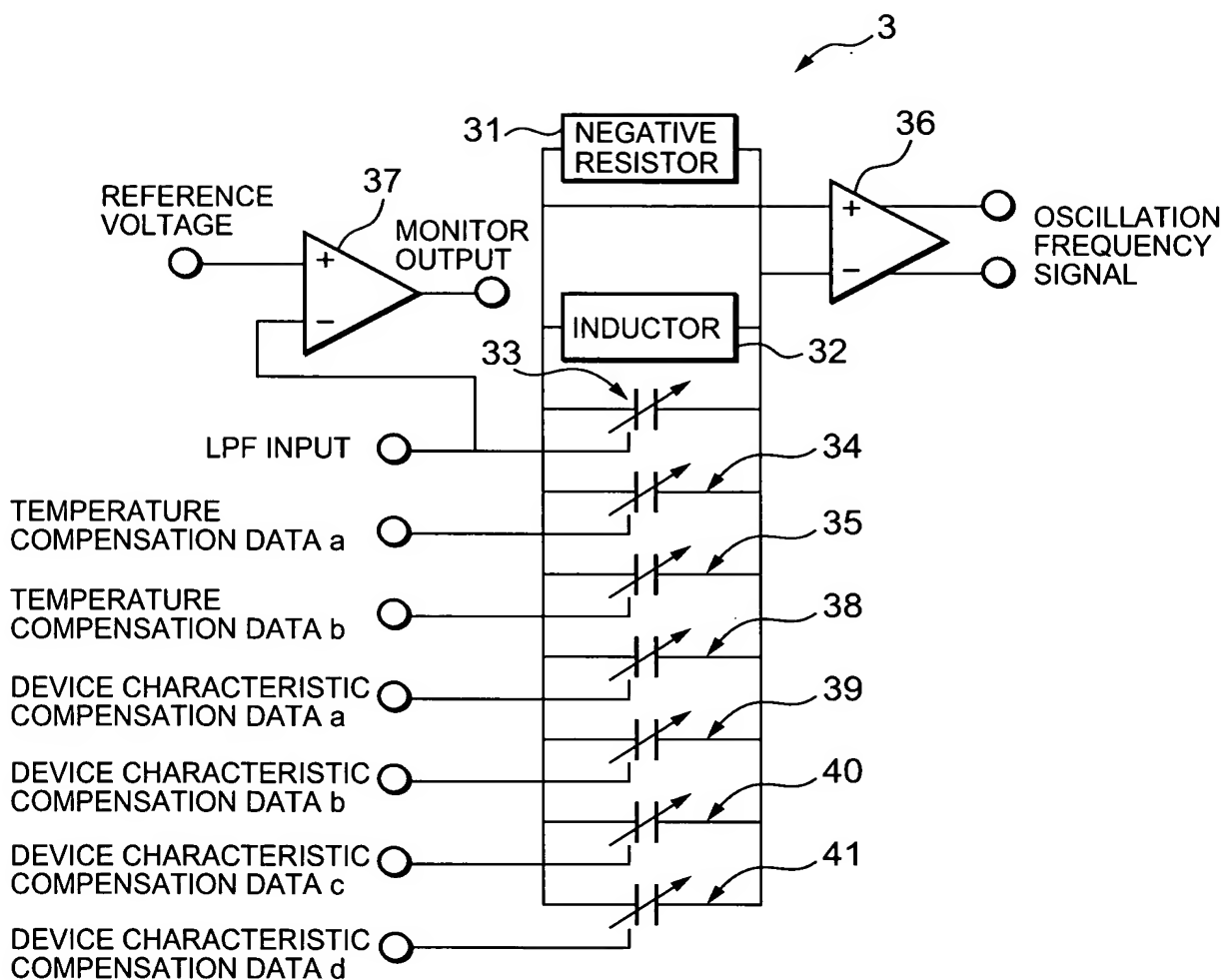


Fig. 9

COMPOSITE CAPACITANCE OF DEVICE CHARACTERISTIC COMPENSATING CAPACITORS	OSCILLATION FREQUENCY (MHz)	AMOUNT OF SHIFT IN OSCILLATION FREQUENCY (MHz)
0	2354	-126
1 * C1	2369	-111
2 * C1	2384	-90
4 * C1	2415	-65
8 * C1	2480	0
9 * C1	2497	17
10 * C1	2514	34
12 * C1	2540	69

Fig. 10



COMPOSITE CAPACITANCE VALUE(*C1)	TRIAL VALUE (INITIAL VALUE)	RESPONSE	TRIAL VALUE (SECOND TIME)	RESPONSE	TRIAL VALUE (THIRD TIME)	RESPONSE	TRIAL VALUE (FOURTH TIME)	RESPONSE	FINAL VALUE
0	(1000)	0	(0100)	0	(0010)	0	(0001)	0	(0000)
2	(1000)	0	(0100)	0	(0010)	0	(0001)	1	(0001)
3	(1000)	0	(0100)	0	(0001)	1	(0011)	0	(0010)
4	(1000)	0	(0100)	0	(0010)	1	(0011)	1	(0011)
5	(1000)	0	(0100)	1	(0110)	0	(0101)	0	(0100)
6	(1000)	0	(0100)	1	(0110)	0	(0101)	1	(0101)
7	(1000)	0	(0100)	1	(0110)	1	(0111)	0	(0110)
8	(1000)	0	(0100)	1	(0110)	1	(0111)	1	(0111)
9	(1000)	1	(1100)	0	(1010)	0	(1001)	0	(1000)
10	(1000)	1	(1100)	0	(1010)	0	(1001)	1	(1001)
11	(1000)	1	(1100)	0	(1010)	1	(1011)	0	(1010)
12	(1000)	1	(1100)	0	(1010)	1	(1011)	1	(1011)
13	(1000)	1	(1100)	1	(1110)	0	(1101)	0	(1100)
14	(1000)	1	(1100)	1	(1110)	0	(1101)	1	(1101)
15	(1000)	1	(1100)	1	(1110)	1	(1111)	0	(1110)
16	(1000)	1	(1100)	1	(1110)	1	(1111)	1	(1111)

Fig. 11

10/11

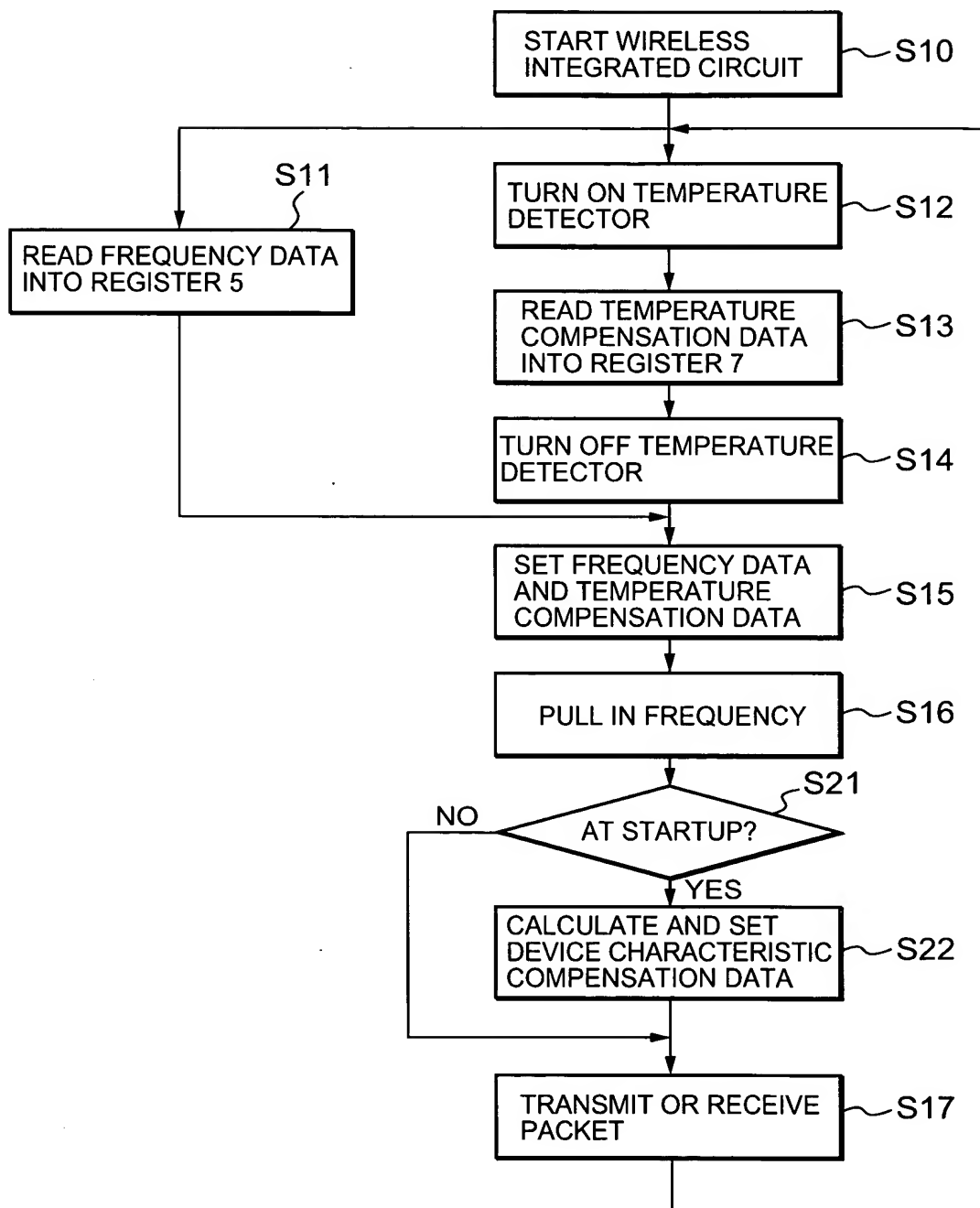


Fig. 12

11/11

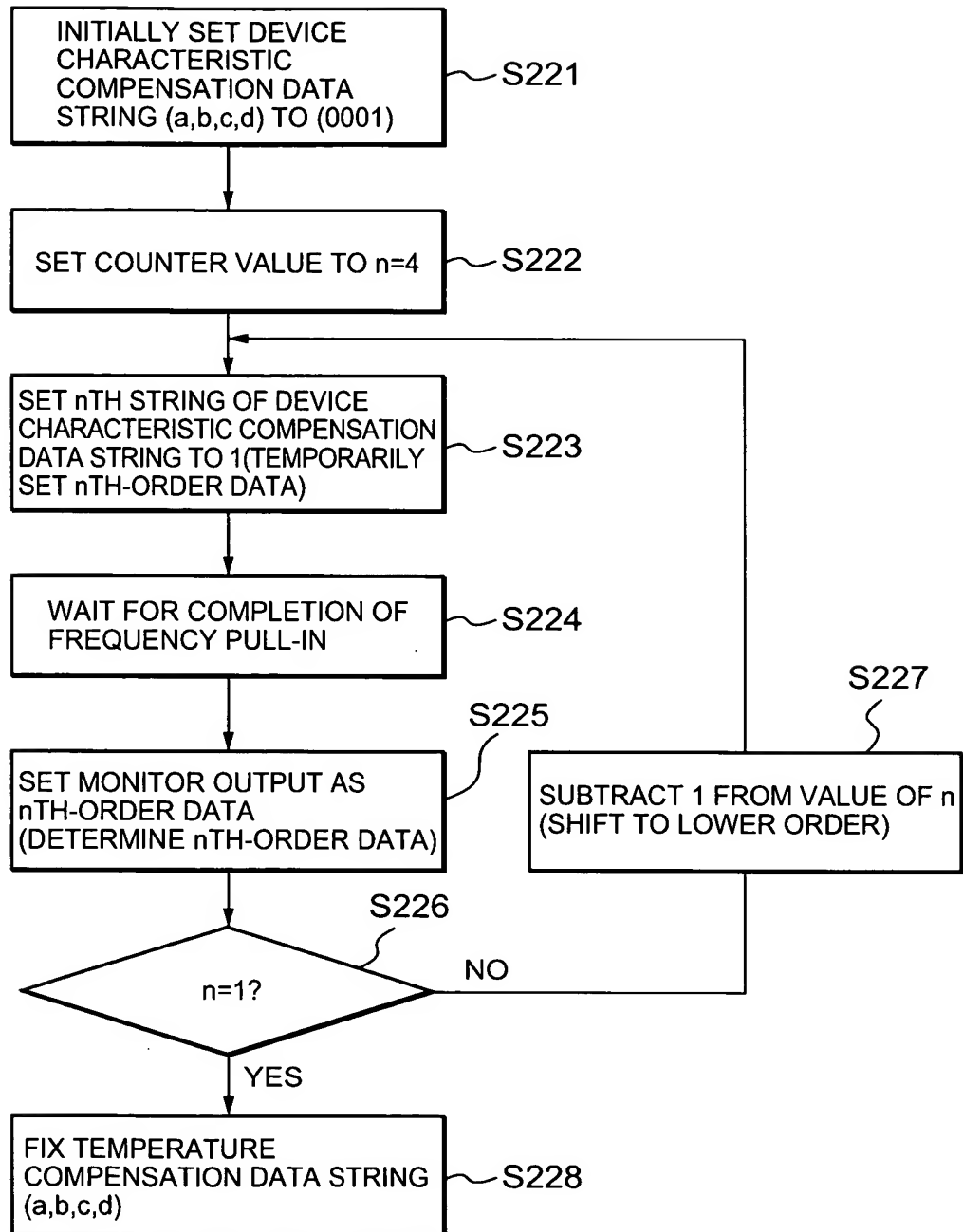


Fig. 13